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FEB 15 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: )  
MORIN ET AL. )  
Serial No. 10/701,165 ) Examiner: S. GEBREMARIAM )  
Confirmation No. 5541 ) Art Unit: 2811 )  
Filing Date: November 4, 2003 )  
For: SEMICONDUCTOR DEVICE WITH MOS )  
TRANSISTORS WITH AN ETCH-STOP LAYER )  
HAVING AN IMPROVED RESIDUAL STRESS )  
LEVEL AND METHOD FOR FABRICATING )  
SUCH A SEMICONDUCTOR DEVICE )

REQUEST FOR EXTENSION OF TIME


COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

Sir:

The Applicants in the above-referenced application respectfully request an extension for filing the response to the Advisory Action mailed January 23, 2006. The final Official Action was mailed August 24, 2005 and a one-month time extension was obtained on December 23, 2005.

The Commissioner is hereby authorized to charge the difference in the fee amount for a three-month extension of time (large entity) in the amount of \$900.00 to the credit card noted in the attached credit card payment form. If any additional extension and/or fee is required, charge Account No. 01-0484.

Respectfully submitted,

  
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407-841-2330 02/16/2006 MBINAS 00000026 10701165

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